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International Researcher IDs

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Publons / Web Of Science ResearcherID: N-5338-2015

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Education Information

Post Doctorate, Universidad Polit cnica de Madrid, Escuela T cnica Superior de Ingenieros de Telecomunicaci n, Departamento de Ingenier a Electr nica, Spain 2022 - 2023

Doctorate, Eskisehir Osmangazi University, Fen Bilimleri Enstit s , Elektrik-Elektronik Anabilim Dalı, Turkey 2015 - 2021

Postgraduate, Eskisehir Osmangazi University, Fen Bilimleri Enstit s , Elektrik-Elektronik M hendisliđi (YI) (Tezli), Turkey 2013 - 2015

Undergraduate, Eskisehir Osmangazi University, M hendislik-Mimarlık Fak ltesi, Elektrik-Elektronik M hendisliđi B l m , Turkey 2006 - 2012

Dissertations

Doctorate, Hızlı Fourier d n ş m n n programlanabilir kapı dizilerinde uygulanması, Eskisehir Osmangazi University, Fen Bilimleri Enstit s , Elektrik-Elektronik Anabilim Dalı, 2021

Postgraduate, Alanda Programlanabilir Kapı Dizileri'xxnde iřaret  retimi ve s zge lerin ger eklenmesi, Eskisehir Osmangazi University, Fen Bilimleri Enstit s , Elektrik-Elektronik M hendisliđi (YI) (Tezli), 2015

Research Areas

Electronic

Academic Titles / Tasks

Assistant Professor, Bilecik Seyh Edebali University, Osmaneli Meslek Y ksekokulu, Elektrik Ve Enerji, 2021 - Continues
Research Assistant, Bilecik Seyh Edebali University, M hendislik Fak ltesi, Elektrik-Elektronik M hendisliđi, 2013 - 2022

Academic and Administrative Experience

Bilecik Seyh Edebali University, 2021 - Continues

Courses

Elektrik ve Elektronik Ölçmeleri, Associate Degree, 2023 - 2024
Sayısal Elektronik, Associate Degree, 2021 - 2022
Alternatif Akım Devre Analizi, Associate Degree, 2021 - 2022
Gömülü Sistemler, Undergraduate, 2018 - 2019

Published journal articles indexed by SCI, SSCI, and AHCI

- I. **Low-Latency 64-Parallel 4096-Point Memory-Based FFT for 6G**
Kaya Z., Garrido M.
IEEE Transactions on Circuits and Systems I: Regular Papers, vol.70, no.10, pp.4004-4014, 2023 (SCI-Expanded)
- II. **Memory-Based FFT Architecture With Optimized Number of Multiplexers and Memory Usage**
KAYA Z., Garrido M., Takala J.
IEEE Transactions on Circuits and Systems II: Express Briefs, vol.70, no.8, pp.3084-3088, 2023 (SCI-Expanded)
- III. **Evaluation of EfficientNet models for COVID-19 detection using lung parenchyma**
Kurt Z., IŞIK Ş., KAYA Z., ANAGÜN Y., Koca N., Çiçek S.
Neural Computing and Applications, vol.35, no.16, pp.12121-12132, 2023 (SCI-Expanded)
- IV. **A novel addressing algorithm of radix-2 FFT using single-bank dual-port memory**
KAYA Z., SEKE E.
Circuit World, vol.48, no.1, pp.64-70, 2022 (SCI-Expanded)

Articles Published in Other Journals

- I. **Memory-Based Self-Ordering FFT for Efficient I/O Scheduling**
Kaya Z., Seke E.
Eskişehir Technical University Journal of Science and and Technology A- Applied Sciences and Engineering, vol.25, no.1, pp.156-167, 2024 (Peer-Reviewed Journal)
- II. **On power transformers energy efficiency based load transfer analysis**
KAYA Z., İMAL N.
International Journal of Energy Applications and Technologies, vol.5, no.3, pp.115-118, 2018 (Peer-Reviewed Journal)
- III. **SINGLE BANK DUAL-PORT MEMORY-BASED FFT FOR FIELD PROGRAMMABLE GATE ARRAYS**
SEKE E., KAYA Z.
Anadolu Üniversitesi Bilim ve Teknoloji Dergisi :A-Uygulamalı Bilimler ve Mühendislik, vol.19, no.4, pp.796-804, 2018 (Peer-Reviewed Journal)

Books & Book Chapters

- I. **Lecture Notes in Networks and Systems**
Kaya Z., Kurt Z., Işık Ş., Koca N.
in: Proceedings of International Conference on Computing and Communication Networks: ICCCN 2021, Ali Kashif Bashir, Giancarlo Fortino, Ashish Khanna, Deepak Gupta, Editor, Springer Nature, Singapore, pp.261-275, 2022

Refereed Congress / Symposium Publications in Proceedings

- I. **Serial-in-Parallel-out FFT**
Seke E., Kaya Z.

5th International Conference on Advanced Technology Sciences (ICAT2017), İstanbul, Turkey, 9 - 12 May 2017, pp.569-571

II. Comparative HDL Implementations of Two Chaotic Generators

Kaya Z., Seke E.

5th International Conference on Advanced Technology Sciences (ICAT2017), İstanbul, Turkey, 9 - 12 May 2017, pp.162-166

III. A Vhdl Implementation of Hénon Map Pseudo-Random Sequence Generator

Kaya Z., Seke E.

International Conference on Engineering and Natural Science, Sarajevo, Bosnia And Herzegovina, 24 - 28 May 2016, pp.668-671

IV. A numerically controlled oscillator based on de Moivre's identity and linear approximation: A ROMless NCO

Kaya Z., Seke E.

25th International Conference on Information, Communication and Automation Technologies, ICAT 2015, Sarajevo, Bosnia And Herzegovina, 29 - 31 October 2015

V. De Moivre Eşitliğini Kullanan Sayısal Kontrollü Osilatör

Kaya Z., Seke E.

2015 23rd Signal Processing and Communications Applications Conference (SIU). IEEE, 2015, Malatya, Turkey, 16 - 19 May 2015, pp.451-454

VI. De Moivre Eşitliği ve Trigonometrik İlişkiler ile Gerçeklenen Sayısal Kontrollü Osilatör

Kaya Z., Seke E.

2015 23rd Signal Processing and Communications Applications Conference (SIU). IEEE, 2015, Malatya, Turkey, 16 - 19 May 2015, pp.1643-1646

VII. Direct generation of upsampled FIR filter response a simple extension to filters with distributed arithmetic

Kaya Z., Seke E.

The IEEE International Conference on Digital Technologies, Zilina, Slovakia, 9 - 11 July 2014, pp.110-113

VIII. İki-Değerli Veri Girişli Raised-Cosine Süzgecinin DDS ile Çarpıcı Kullanılmadan Gerçeklenmesi

Kaya Z., Seke E.

2014 22nd Signal Processing and Communications Applications Conference (SIU). IEEE, 2014, Trabzon, Turkey, 23 - 25 April 2014, pp.128-131

Supported Projects

KAYA Z., Realizing Advanced FFT Implementations for 6G, 2022 - Continues

KAYA Z., Project Supported by Higher Education Institutions, Yazılım Tanımlı Radyo Bileşenleri Geliştirilmesi, 2016 - 2018